

10/030175

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FILED UNDER 35 U.S.C. 371

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

9.

APPL NUM 10030175	FILING DATE 01/31/2002	CLASS 438	SUBCLASS 2812	GAU 2812	EXAMINER PERALTA
**APPLICANTS: Deleonibus Simon;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/FR01/01776 06/08/2001					
**FOREIGN APPLICATIONS VERIFIED: FRANCE 00/07419 06/09/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 218207US2PCT	
TITLE : Method for making an electronic component with self-aligned drain and gate, in damascene architecture					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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